



Department of Computer Systems Engineering
 Time Table Fall 2012 (3rd Semester)
 wef September 03, 2012

3rd Semester- Batch-13

DAYS	Regular	8:00-9:15	9:30-10:45	11:00-12:15	12:30-01:45
Monday	A	LA (CR-6)	CS-II (CR-3)	DE (CR-3)	OOP (CR-6)
	B	DE (CR-3)	LA (CR-6)	OOP (CR-6)	DLD (CR-3)
Tuesday	A	DE (CR-3)	CS-II (CR-2)	LA (CR-6)	DLD (CR-6)
	B	LA (CR-6)	OOP (CR-6)	OOP LAB (LAB-3)	
Wednesday	A	DLD (CR-3)	OOP (CR-6)	OOP LAB (LAB-3)	
	B	DE (CR-2)	CS-II (CR-2)	DLD (CR-2)	
Thursday	A		DLD LAB (MP LAB)		
	B		CS-II (CR-2)	CS-II LAB (EE-LAB)	
Friday	A		CS-II LAB (EE-LAB)		
	B	DLD LAB (MP LAB)			

Course Abr	Course Code	Course Title	Instructors
DLD	CSE-202	Digital Logic Design	Dr. Laiq Hasan
DLD LAB	CSE-202 L	Digital Logic Design Lab	Engr. Fahad Ullah
OOP	CSE-201	Data Structure with OOP	Dr. Nasru Minallah
OOP LAB	CSE-201 L	Data Structure with OOP Lab	Engr. Madiha Sher
CS-II	CSE-203	Circuits and Systems-II	Prof. Aftab Ahmad
CS-II LAB	CSE-203 L	Circuits and Systems-II Lab	Engr. Arif Ali Shah
LA	BSI-111	Linear Algebra	Mr. Attau rehman
DE	BSI-231	Differential Equations	Dr. Rehan Ali Shah

Semester Coordinator